# Qualcom

RF360 Europe GmbH

### **SAW** components

### SAW RF filter

Short range devices

Series/type:B4344Ordering code:B39921B4344P810

Date:September 20, 2016Version:2.1

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# ⊗TDK

915.00 MHz

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### 1 Application

- Low-loss RF filter for remote control receivers
- No matching network required for operation at 50Ω
- Usable pass band 26MHz

### 2 Features

- Package size 1.4±0.1 mm × 1.1±0.1 mm
- Package height 0.45 mm (max.)
- Package code QCS5P
- Approximate weight 3 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Filter surface passivated
- AEC-Q200 qualified component family (Grade 3: -40 °C to +85 °C)
- Electrostatic Sensitive Device (ESD)



**Figure 1:** Picture of component with example of product marking.



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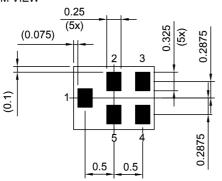
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### 3 Package

BOTTOM VIEW



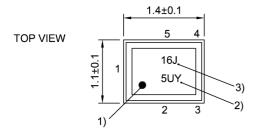
Pad and pitch tolerance ±0.05

### 4 Pin configuration

- ∎ 1 Input
- ∎ 4 Output
- 2, 3, 5 Ground

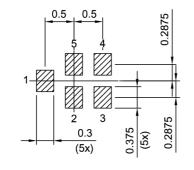
#### SIDE VIEW

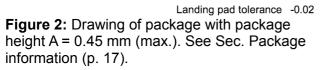




- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number









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### 5 Matching circuit

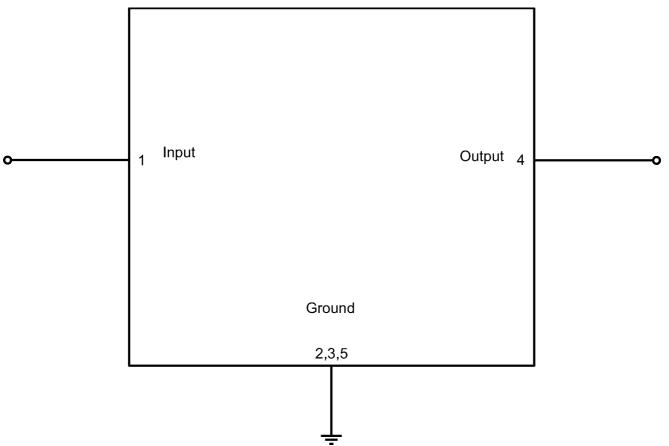


Figure 3: Schematic of matching circuit. No external matching components required.



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### 6 Characteristics

Temperature range for specification	T <sub>SPEC</sub>	= −40 °C +85 °C
Input terminating impedance	Z	= 50 Ω
Output terminating impedance	Z <sub>OUT</sub>	= 50 Ω

Characteristics				min. for $T_{_{\rm SPEC}}$	<b>typ.</b> @+25 °C	max. for T <sub>SPEC</sub>	
Center frequency			f <sub>c</sub>	—	915	—	MHz
Maximum insertion attenuation			$\alpha_{_{max}}$				
	902 928	MHz		_	2.8	4.0	dB
Amplitude ripple (p-p)			Δα				
	902 928	MHz		—	1.7	2.8	dB
Maximum VSWR			$VSWR_{max}$				
@ input port	902 928	MHz		_	1.8	2.1	
@ output port	902 928	MHz		_	1.8	2.1	
Minimum attenuation			$\alpha_{_{min}}$				
	10 824	MHz		40	48	_	dB
	824 890	MHz		28	37	_	dB
	890 894	MHz		12	34	—	dB
	941 960	MHz		15	22	—	dB
	960 1350	MHz		32	41	—	dB
	1350 2000	MHz		35	40	—	dB



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### 7 Maximum ratings

Operable temperature	T <sub>op</sub> = −40 °C +85 °C	
Storage temperature	<i>T</i> <sub>STG</sub> <sup>1)</sup> = −40 °C +85 °C	
DC voltage	$ V_{\rm DC} ^{2)} = 0 V$	
Input power @ input port	P <sub>IN</sub> = 15 dBm	

<sup>1)</sup> Not valid for packaging material. Storage temperature for packaging material is -25 °C to +40 °C.

<sup>2)</sup> In case of applied DC voltage blocking capacitors are mandatory.



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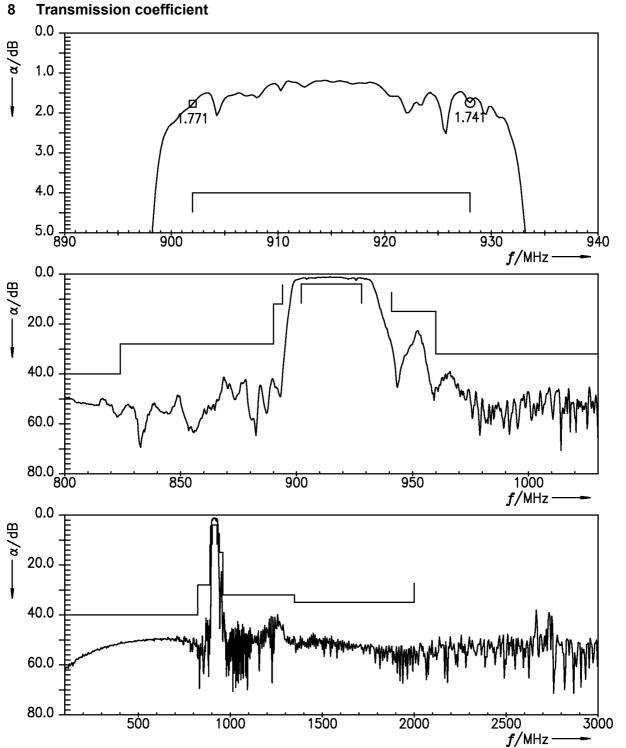


Figure 4: Attenuation.



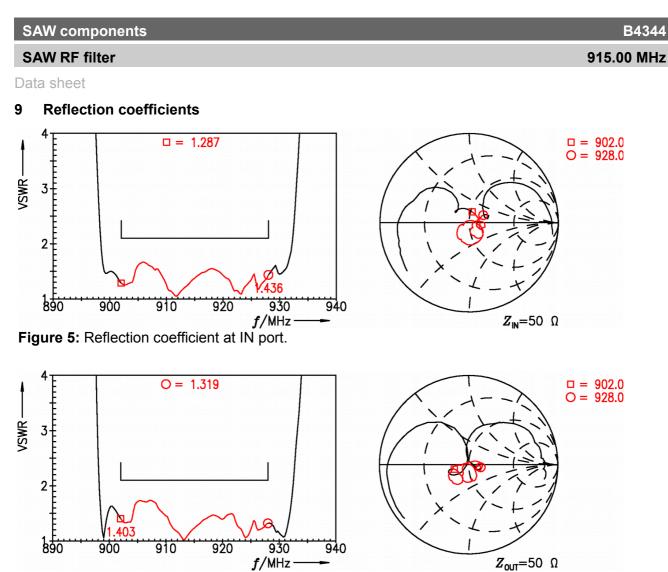


Figure 6: Reflection coefficient at OUT port.

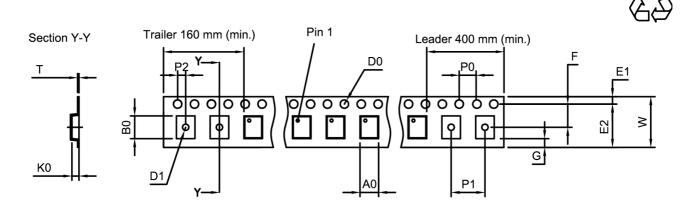


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### 10 Packing material

10.1 Tape



User direction of unreeling

Figure 7: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A <sub>0</sub>	1.27±0.05 mm
B <sub>0</sub>	1.57±0.05 mm
$D_0$	<b>1.5</b> +0.1/-0 mm
D <sub>1</sub>	0.5±0.1 mm
E1	1.75±0.1 mm

 Table 1: Tape dimensions.

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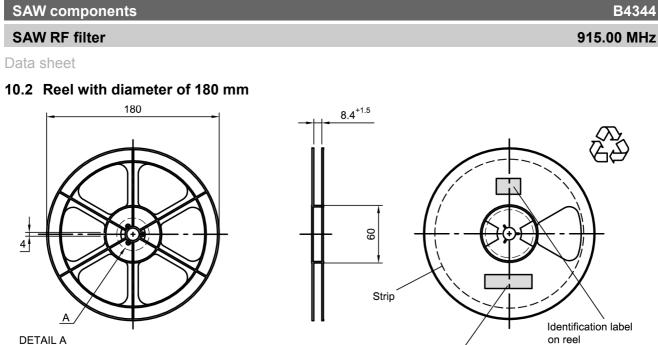
E2	6.25 mm (min.)
F	3.5±0.05 mm
G	0.75 mm (min.)
K <sub>0</sub>	0.62±0.05 mm
P <sub>0</sub>	4.0±0.1 mm

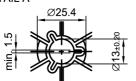
P <sub>1</sub>	4.0±0.1 mm
$P_2$	2.0±0.05 mm
Т	0.25±0.03 mm
W	8.0+0.3/-0.1 mm

## \_

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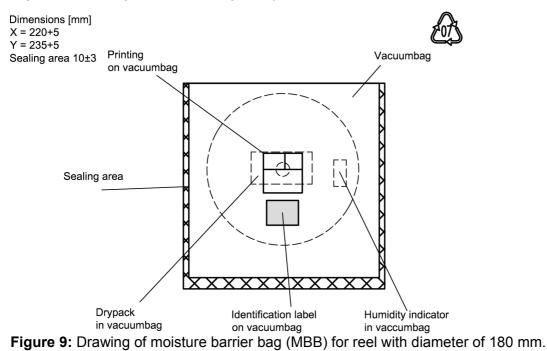




Surface resistivity < 10<sup>12</sup> Ohms/sq

ESD label on reel

Figure 8: Drawing of reel (first-angle projection) with diameter of 180 mm.



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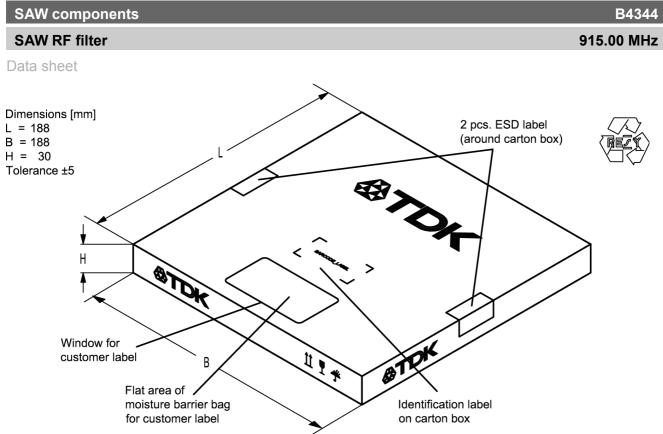


Figure 10: Drawing of folding box for reel with diameter of 180 mm.



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1 Markin	ng							
Products a	are marked	with produc	ct type num	ber and	lot number	r encoded a	according to	Table 2:
Type nur		•	51				0	
•••								
	git type num ed by a spe		0	,	jit marking.	e.g., I	B3xxxxB <u>123</u>	<u>34</u> xxxx,
Example	of decodin	g type num	ber markin	g on de	vice		in decimal	code.
	16J			-	=>	•	12:	34
	<b>1</b> x 32	2 <sup>2</sup> + <b>6</b> x 32 <sup>1</sup>	+ 18 (=J) >	( 32º	=		123	34
The BAS	SE32 code f	or product	type B4344	is 47R.				
Lot num	ber:							
The last	5 digits of t	he lot numb	ber.			e.g.,	123	345.
	ded based			code inf	to a 3 digit i			)
	of decodin	•			•	Ū	in decimal	code
Example	5UY	giotinamot	an manning		=>			345
		7 <sup>2</sup> + <b>27 (=U</b> )	) x 47 <sup>1</sup> + <b>31</b>	(=Y) x				345
Adopte	d PASE22 of	ada far tuna i	aumhar		Adop	tod BASE47	anda far lat n	umbor
Decimal	Base32 co	Decimal	Base32	-	Decimal	Base47	code for lot n Decimal	Base47
value	code	value	code		value	code	value	code
0	0	16	G	-	0	0	24	R
1	1	17	н	_	1	1	25	S
2	2	18	J	_	2	2	26	Т
3	3	19	к	_	3	3	27	U
4	4	20	М	_	4	4	28	V
5	5	21	N	_	5	5	29	W
6	6	22	Р	_	6	6	30	X
7	7	23	Q	_	7	7	31	Y
8	8	24	R	_	8	8	32	Z
9	9	25	S	_	9	9	33	b
10	A	26	Т	_	10	A	34	d
11	B	27	V	_	11	B	35	f
12	С	28	W	_	12	С	36	h

Table 2: Lists for encoding and decoding of marking.

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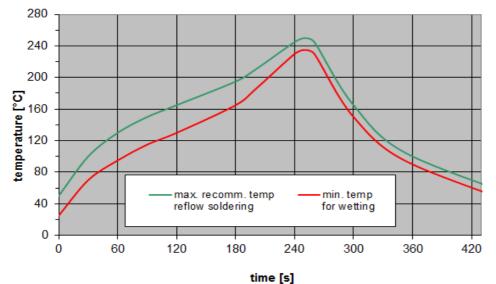
Data sheet

### 12 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3<sup>rd</sup> edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
<i>T</i> > 220 °C	30 s to 70 s
<i>T</i> > 230 °C	min. 10 s
<i>T</i> > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature $T_{peak}$	250 °C +0/-5 °C
wetting temperature T <sub>min</sub>	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

**Table 3:** Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).



**Figure 11:** Recommended reflow profile for convection and infrared soldering – lead-free solder.

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### **13 ESD protection of SAW filters**

SAW filters are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3<sup>rd</sup> order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

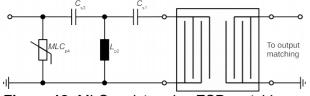


Figure 12: MLC varistor plus ESD matching.

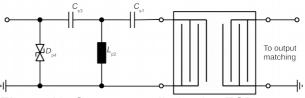
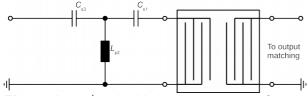


Figure 13: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.



**Figure 14:** 3<sup>rd</sup> order high-pass structure for basic ESD protection.

In all three figures the shunt inductor  $L_{p2}$  could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report: **"ESD protection for SAW filters"**. This report can be found under <u>www.epcos.com/rke</u>. Click on "Applications Notes".



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### 14 Annotations

### 14.1 Matching coils

See TDK inductor pdf-catalog <u>http://www.tdk.co.jp/tefe02/coil.htm#aname1</u> and Data Library for circuit simulation <u>http://www.tdk.co.jp/etvcl/index.htm</u>.

### 14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

### 14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.

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### 15 Cautions and warnings

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### 15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

### 15.3 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

### 15.4 Package information

### Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

### Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

### **Projection method**

Unless otherwise specified first-angle projection is applied.



### Important notes

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