SPECIFICATIONS			
CUSTOMER . SAMPLE CODE (Ver.) .			
MASS PRODUCTION CODE (Ver.)	PC1602LRS-FWA-B-Q (Rev.0)		
DRAWING NO. (Ver.)	PC-95003		

Customer Approved

Date:

Approved	QC Confirmed	Designer
技術部 2005年28 建筑法		AAS/12Absa

- Approval For Specifications Only.
 - * This specification is subject to change without notice.

Please contact Powertip or it's representative before designing your product based on this specification.

Approval For Specifications and Sample.

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RECORDS OF REVISION

Date	Rev.	Description	Note	Page
2005/10/28	0	PC1602LRS-FWA-B-Qis the ROHS compliant part number based on Powertip's standard PC1602LRS-FWA-B		

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Note: For detailed information please refer to IC data sheet: ST7066U,KS0065B



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Gray Positive Transflective Normal Temp.
Driver Condition	LCD Module: 1/16 Duty, 1/5 Bias
Viewing Direction	6 O'clock
Backlight	YG LED B/L
Weight	36 g
Interface	_
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web side :
	http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	84.0(L) * 44.0(w) * 13.7(H)(Max)	mm
Viewing Area	61.0(L) * 15.8(w)	mm
Active Area	56.21(L) * 11.5(w)	mm
Dot Size	0.56(L) * 0.66(w)	mm
Dot Pitch	0.60 (L) * 0.70(w)	mm

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	$ m V_{DD}$	_	-0.3	7.0	V
LCD Driver Supply Voltage	V_{LCD}		VDD-10.0	V _{DD} +0.3	V
Input Voltage	$V_{\rm IN}$		-0.3	V _{DD} +0.3	V
Operating Temperature	T_{OP}	Excluded B/L	0	50	$^{\circ}$
Storage Temperature	T_{ST}	Excluded B/L	-20	70	$^{\circ}\mathbb{C}$
Storage Humidity	H_D	Ta<40 ℃	-	90	%RH



1.4 DC Electrical Characteristics

 $V_{DD}\!=5.0~V\pm0.5V$, $V_{SS}\!=0V$, $Ta=25^{\circ}\!C$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	$V_{ m DD}$	_	4.5	5.0	5.5	V
"H" Input Voltage	V_{IH}	_	0.7 Vdd	-	VDD	V
"L" Input Voltage	V_{IL}	_	-0.3	-	0.6	V
"H" Output Voltage	V_{OH}	Iон=-0.1mА	3.9	-	VDD	V
"L" Output Voltage	$V_{ m OL}$	IOL=0.1mA	-	ı	0.4	V
Supply Current	I_{DD}	$V_{DD} = 5.0 \text{ V}$	-	1.5	3.0	mA
		$0^{\circ}\!\mathbb{C}$	-	-	-	
LCM Driver Voltage	$ m V_{OP}$	25°C*1	4.3	4.5	4.7	V
		50°C	-	-	-	

Note: *1. THE V_{OP} TEST POINT IS V_{DD} - V_{O} .

1.5 Optical Characteristics

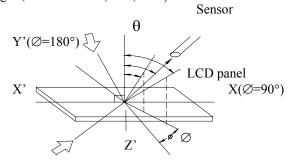
LCD Panel : 1/16 Duty + 1/4 Bias + V_{LCD} =4.2 V + Ta = 25 $^{\circ}\text{C}$

Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	C≥2.0, Ø = 0°	40°	-	-	Notes 1 & 2
Contrast Ratio	С	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	5	7	-	Note 3
Response Time(rise)	tr	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	-	150 ms	-	Note 4
Response Time(fall)	tf	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	-	300 ms	-	Note 4



Note 1: Definition of angles θ and \emptyset

Light (when reflected) $z (\theta=0^{\circ})$



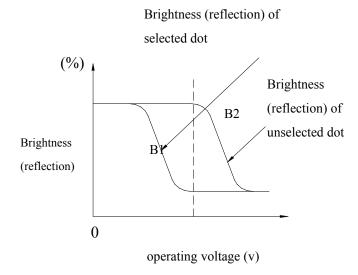
Light (when transmitted) $Y(\varnothing=0^{\circ})$ $(\theta=90^{\circ})$

Note 3: Definition of contrast C

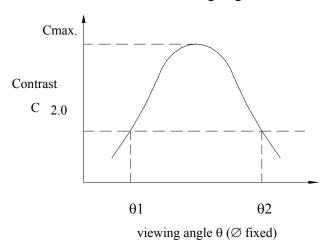
C = -

Brightness (reflection) of unselected dot (B2)

Brightness (reflection) of selected dot (B1)

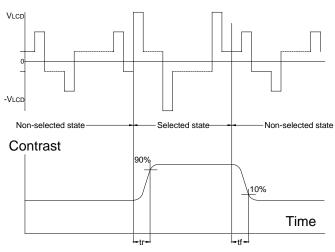


Note 2: Definition of viewing angles $\theta 1$ and $\theta 2$



Note: Optimum viewing angle with the naked eye and viewing angle θ at Cmax. Above are not always the same

Note 4: Definition of response time



Note: Measured with a transmissive LCD panel which is displayed 1 cm²

$$\begin{split} V_{LCD} : Operating \ voltage & f_{FRM} : Frame \ frequency \\ t_r & : Response \ time \ (rise) & t_f : Response \ time \ (fall) \end{split}$$



1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°℃	-	300	mA
Reverse Voltage	VR	Ta =25°℃	-	8	V
Power Dissipation	PO	Ta =25°℃	-	1.38	W

Electrical / Optical Characteristics

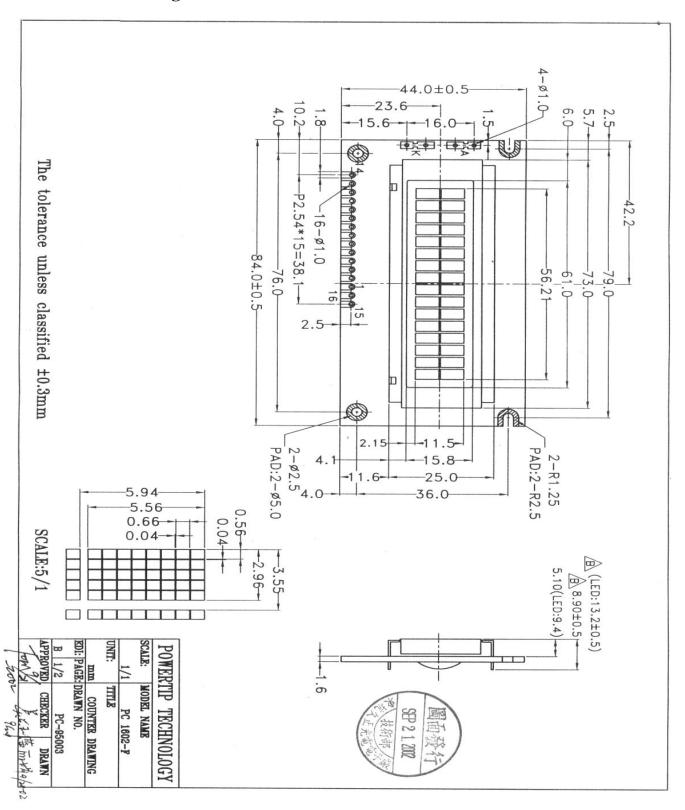
Ta =25°C

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 120 mA	-	4.2	4.6	V
Reverse Current	IR	VR= 8 V	-	-	0.2	mA
Wavelength	λр	IF= 120 mA	571	-	576	nm
Luminous Intensity (without LCD)	IV	IF=120 mA	160	210	250	cd/m ²
Color	Yellow-green					

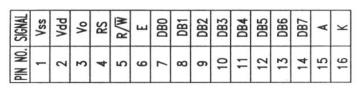


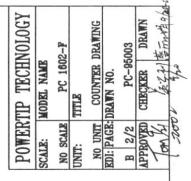
2. MODULE STRUCTURE

2.1 Counter Drawing

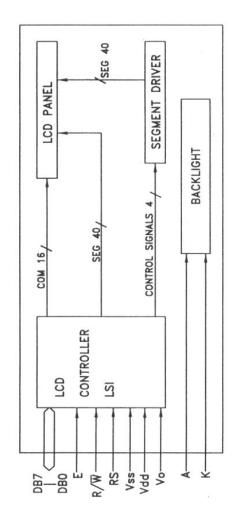










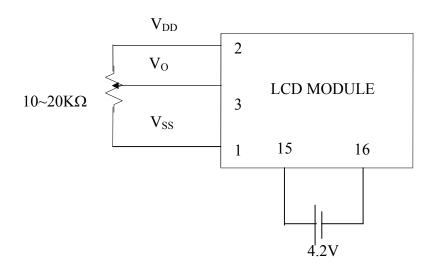




2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	V_{SS}	Power Supply (Vss=0)
2	$V_{ m DD}$	Power Supply (V _{DD} >V _{SS})
3	V_{O}	Operating voltage for LCD
		Register Selection input
4	RS	High = Data register
4	KS	Low = Instruction register (for write)
		Busy flag address counter (for read)
		Read/Write signal input is used to select the read/write
5	R/W	mode
		High = Read mode, Low = Write mode
6	Е	Start enable signal to read or write the data
		Four low order bi-directional three-state data bus lines. Use
7~10	$\mathrm{DB0}\sim\mathrm{DB3}$	for data transfer between the MPU and the LCD module.
		These four are not used during 4-bit operation.
		Four high order bi-directional three-state data bus lines.
		Used for data transfer between the MPU and the LCD
11~14	DB4~DB7	module.
		DB7 can be used as a busy flag.
15	A	Power supply for LED B/L (+)
16	K	Power supply for LED B/L (-)

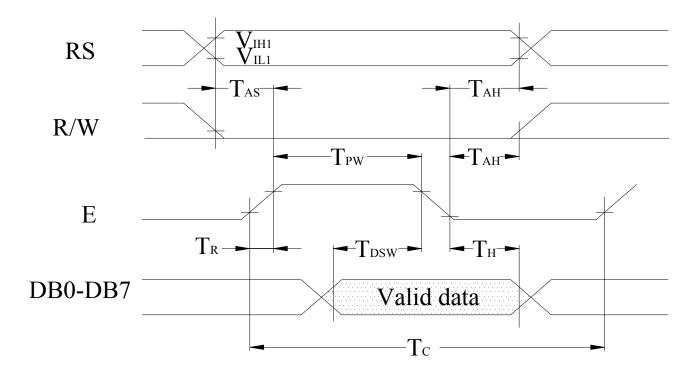
Contrast Adjust



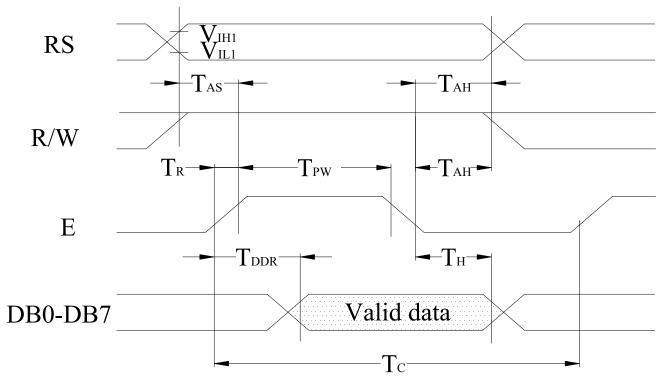


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



●Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

 $(V_{cc} = +5V, Ta=25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T_{C}	Enable Cycle Time	Pin E	1200	1	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DSW}	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
T_{H}	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

• Read Mode (Reading data from ST7066U to MPU)

 $(V_{cc} = +5V, Ta=25^{\circ}C)$

				,		, ,
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T_{C}	Enable Cycle Time	Pin E	1200	1	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	1	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T_{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T_{H}	Data Hold Time	Pins:DB0~DB7	10	-	-	ns



2.4 Display Command

					Instru	iction	Code	;				Description
Instructions	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0	Description	Time (270KHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	×	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37118
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	×	×	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	37µs
Function Set	0	0	0	0	1	DL	N	F	×	×	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37µs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37µs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37µs



Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Refer to Instruction Table for the list of each instruction execution time .



2.5 Character Pattern

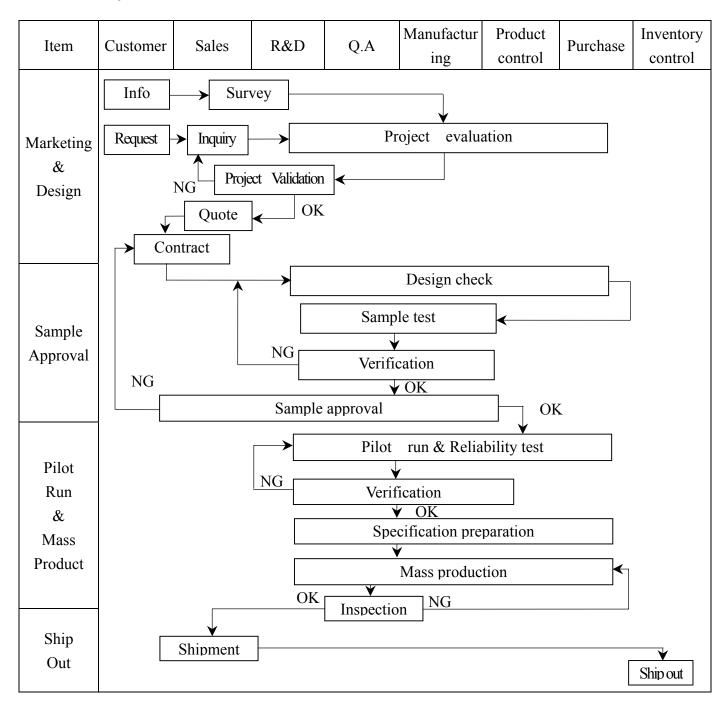
■ CHARACTER PATTERN(SO/HO/EA,WA)

Lower 4 Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)					-	٠.						-51	≡ .		-
xxxx0001	(2)		1	1			-===	-==			===		====	£		
xxxx0010	(3)		::				-	 -			Ē			_::: ¹		=
xxxx0011	(4)		#			===	=	::::-				=====	-	===	:E.	::-:
xxxx0100	(5)			:: :].				ŧ			٠.		!	-		===
xxxx0101	(6)		::-: <u>:</u> :	===			::::	II			::	:	:=			
xxxx0110	(7)			€.	-	I.,.I	-{···	i:								:
xxxx0111	(8)		።					1,.,1			-:::		[:-:"			H
xxxx1000	(1)					::: <u>:</u>]×:[<u> </u> -	-:]]		! _!	-,I"	[:::
xxxx1001	(2)		<u>;</u> ;		I	٠	1	-:::				-"][i i	:	
xxxx1010	(3)		:-[-:	:	I		 :	::::					: ·-	<u>.</u>	. j	=
xxxx1011	(4)			ij.		-	! -::	4			:=[-	!!	<u> </u>		:-:	; =
xxxx1100	(5)		:=	-: <u>`</u>			1	i			-[-:-	:::			4	
xxxx1101	(6)						[**]	3-			.::.	::	٠٠.	 	₩.	:
xxxx1110	(7)		::	>		····]-";							-,*-	ñ	
xxxx1111	(8)			:							: :.:	·!	-::	===		

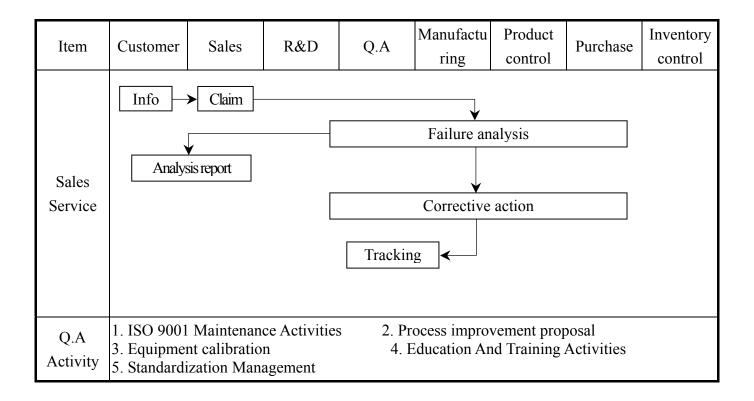


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



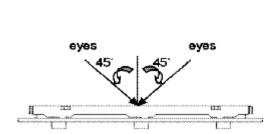


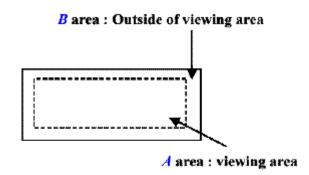




3.2 Inspection Specification

- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge · MIL-STD · Powertip Tester · Sample
- ◆Defect Level: Major Defect AQL 0.4; Minor Defect AQL 1.5.
- ◆OUT Going Defect Level: Sampling.
- ◆Manner of appearance test:
 - (1). The test be under 40W×2 fluorescent light 'and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (3). Definition of area . (Fig. 2)





◆ Specification:

NO	Item	Criterion	level				
		1.1 The part number is inconsistent with work order of Production.	Major				
01	Product condition	1.2 Mixed production types.	Major				
		1.3 Assembled in inverse direction.	Major				
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major				
		4.1 Missing line character \ dot and icon.					
		4.2 No function or no display.	Major				
04	Electrical Testing	4.3 Output data is error.					
		4.4 LCD viewing angle defect.					
		4.5 Current consumption exceeds product specifications.	Major				
05	Black or white dot \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	 5.1 Round type: 5.1.1 display only: • White and black spots on display ≤ 0.25mm, no more than Four white or black spots present. • Densely spaced: NO more than two spots or lines within 3mm 	Minor				



◆Specification:

NO	Item	Criterion					level	
05	Black or white dot \ scratch \ contamination Round type \ \rightarrow \frac{x}{y} \ \Phi = (x+y)/2	5.1.2 Nom-o Din 0.2 5.1.3 Line ty	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			Acceptance(Q'ty) Accept no dense 3 2 4 Acceptance (Q'ty) A area B area Accept no dense Don't count Don't count As round type		
	11		w>0.075mi	n	ASTO	ound type		
06	Polarizer Bubble	$\Phi \leq \frac{0.20 \text{mm}}{0.50 \text{mm}} < \frac{0.50 \text{mm}}{0.50 \text{mm}$	(diameter : Φ) ≤ 0.20 mm $< \Phi \leq 0.50$ mm $< \Phi \leq 1.00$ mm > 1.00 mm		Acceptance area ept no dense 3 2 0 4	(Q'ty) B area Don't count Don't count Don't count Don't count Don't count	Minor	
07	The crack of glass	From Bac	$x = \frac{X}{1/5}$	X	le terminal : $\frac{Y}{Y} = \frac{Y}{1/2 D}$ Neglect	$egin{array}{c} Z \ Z \leq t \end{array}$	Minor	



◆Specification:

	ecification:				
NO	Item	Criterion			Level
		• Glass Crack: 7.2 General glass crac 7.2.1	X De L	Z	
	The crack of glass	X	Y	Z	Minor
	X: The length of Crack	Neglect	Out A area	Neglect	
	Y: The width of crack	7.2.2	********		
07	Z: The thickness of crack		· · · · · · · · · · · · · · · · · · ·		
	D: terminal length	X Neglect	Y Out A area	Z Neglect	
	T: The thickness of glass	Negicci	Out A area	Negicci	
	A: The length of glass	7.3 Glass remain:			
				Y 1/3 d	Minor



◆Specification:

NO	ecification : Item	Criterion			Level
07	The crack of glass X: The length of Crack Y: The width of crack Z: The thickness of crack D: terminal length T: The thickness of	7.4 Corner cra	ack and medial crack:	X SP	Minor
	glass	X	Y	Z	
	A: The length of glass	≤1/5a	Crack can't enter viewing area	≤1/2t	_
	giass	≤1/5a	Crack can't exceed the half of width of SP width of SP	$1/2t < Z \le 2t$	
		8.1 Backlight c	an't work normally.		Major
08	Backlight elements	8.2 Backlight o	loesn't light or color is wrong.		Major
00	Ciements	8.3 Illuminatio	n source flickers when lit.		Major
		9.1 pin type mu	ust match type in specification she	et	Major
		9.2 No short ci	rcuits in components on PCB or F.	PC	Major
	General	9.3Product pac	kaging must the same as specified	on	
09	appearance	packaging	specification sheet.		Major
		9.4 The folding	g and peeled off in polarizer are no	t	M .
		acceptable			Major
		9.5 The PCB	or FPC between B/L assembled d	istance	
		(PCB or FF	PC) is ≤ 1.5 mm		Major



4. RELIABILITY TEST

4.1 Reliability Test Condition

1	Renability Test Colluition						
NO.	TEST ITEM	TEST CONDITION					
1	High Temperature Storage Test	Keep in 70 $\pm 2^{\circ}$ C 96 hrs					
		Surrounding temperature, then storag	e at normal condition 4hrs				
2	Low Temperature Storage Test	Keep in $-20 \pm 2^{\circ}$ C 96 hrs					
		Surrounding temperature, then storage at normal condition 4hrs					
		Keep in $+60^{\circ}$ C/90%RH duration for 9					
		Surrounding temperature, then storag	e at normal condition 4hrs				
3	High Humidity Storage	(Excluding the polarizer)Or)				
	Tright frumdity Storage	Keep in +40°C/90%RH duration for 9					
		Surrounding temperature, then storag					
4	Vibration Test	1. Sine wave $10 \sim 55$ HZ frequency	` ,				
	violation rest	2. The amplitude of vibration :1.5 n					
		3. Each direction (XYZ) duration for	or 2 Hrs				
		Air Discharge:	Contact Discharge:				
		Apply 6 KV with 5 times	Apply 250V with 5 times				
		Discharge foreach polarity +/-	discharge foreach polarity +/-				
		1. Temperature ambinace:15°C ~35	$^{\circ}$ C				
		2. Humidity relative: 30% ~ 60%					
5	ESD Test	3. Energy Storage Capacitance(Cs+Cd):150pF±10%					
		4. Discharge Resistance(Rd):330 Ω±10%					
		5. Discharge, mode of operation:					
		Single Discharge (time between successive discharges at least 1 s)					
		(Tolerance If the output voltage indication: ±5%)					
		$-20^{\circ} \text{C} \rightarrow 25^{\circ} \text{C} \rightarrow 70^{\circ} \text{C}$	$C \rightarrow 25^{\circ}C$				
	Tommoretum Cristina Tark	(30mins) (5mins) (3	0mins) (5mins)				
6	Temperature Cycling Test	(30mins) (5mins) (3	le •				
		Surrounding temperature, then storag					
		1. Sine wave 10~55HZ frequency (
7	Vibration Test (Packaged)						
		2. The amplitude of vibration :1.5 mm3. Each direction (XYZ) duration for 2 Hrs					
			Drop Height (cm)				
		0~45.4	122				
		45.4 ~ 90.8	76				
8	Drop Test (Packaged)	90.8 ~ 454	61				
		Over 454	46				
		Drop direction : **3 comer	/1 edges /6 sides etch 1times				
	<u> </u>	<u> </u>					



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.